# EAST Search History

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|---|---|---|---|---|---|---|
| L1 | 1166 | processors (identification or ID) memory stor$5 control stall (data adj path$2) | US-PGPUB; USPAT; USOCR; IBM_TDB | AND | OFF | 2007/03/01 14:56 |
| L2 | 206 | processors (identification or ID) memory stor$5 control stall (data adj path$2) (circuit adj board) | US-PGPUB; USPAT; USOCR; IBM_TDB | AND | OFF | 2007/03/01 14:56 |
| L3 | 135 | processors (identification or ID) memory stor$5 control stall (data adj path$2) (circuit adj board) (data adj flow) | US-PGPUB; USPAT; USOCR; IBM_TDB | AND | OFF | 2007/03/01 14:56 |
| L4 | 0 | processors (identification or ID) memory stor$5 control stall (data adj path$2) (circuit adj board) (data adj flow) (setting adj data) | US-PGPUB; USPAT; USOCR; IBM_TDB | AND | OFF | 2007/03/01 14:56 |
| L5 | 6501 | (semiconductor adj substrate) (circuit adj board) | US-PGPUB; USPAT; USOCR; IBM_TDB | AND | OFF | 2007/03/01 14:56 |
| L6 | 1 | (semicoductor adj substrate) (circuit adj board) | US-PGPUB; USPAT; USOCR; IBM_TDB | AND | OFF | 2007/03/01 14:56 |
| L7 | 3579 | (semiconductor adj substrate) (circuit adj board) | USPAT | AND | OFF | 2007/03/01 14:56 |